

| Class | Subclass |
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| ISSUE CLASSIFICATION | |

PATENT NUMBER

U.S. UTILITY Patent Application

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| <p>I.P.E.</p> <p>SCANNED <i>TR4</i> O.A. <i>HT</i></p> | <p>PATENT DATE</p> |
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| APPLICATION NO. 09/895933 | CONT/PRIOR | CLASS 716 200 | SUBCLASS 512 | ART UNIT 2823 2822 | EXAMINER SCOTT |
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APPLICANTS

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ARTICLE

Membrane switch circuit layout and method for manufacturing

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

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|---|--|-------------|------------|-----------------------------------|----------------------|
| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| | | | | | |
| <input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ | _____ (Primary Examiner) (Date) | | | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | | ISSUE BATCH NUMBER | |
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